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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Issue Application for:

Zheng et al.

Application No.: 10/611,837

Filed: June 30, 2003

Patent No.: 6,806,204

Issued: October 19, 2004

For: SEMICONDUCTOR ETCH SPEED
MODIFICATION

Examiner: Amir Zarabian

Art Group: 2822

Confirmation No.: 5152

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Typed or Printed: Yvette L. Chiscaden

Signature:

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FEB 01 2007
of Correction*

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Alexandria, VA 22313-1450

REQUEST FOR CERTIFICATE OF CORRECTION

Dear Sir:

Upon review of the above-referenced Letters Patent, Applicant noted errors that
are the mistake of the USPTO. Those errors are as follows:

Column 16

Lines 9-10, "...conforming-passivation..." should read --...

conforming passivation...--.

Line 21, "...patterned. etch..." should read --...patterned etch...--.

FEB - 5 2007

Enclosed is a Certificate of Correction (in duplicate) and it is respectfully requested that a Certificate of Correction be issued. This request for correction is made under the provisions of 37 CFR 1.323.

Respectfully submitted,
SCHWABE, WILLIAMSON & WYATT, P.C.



Al AuYeung
Reg. No. 35,432

Dated: January 25, 2007
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Portland, Oregon 97204
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Attorney's Docket No.: 110578-132742
Patent No.: 6,806,204

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTIONPage 1 of 1

PATENT NO. : 6,806,204

APPLICATION NO. : 10/611,837

ISSUE DATE : October 19, 2004

INVENTOR(S) : Jun-Fei Zheng and Jesper Hanberg

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

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MAILING ADDRESS OF SENDER (Please do not use customer number below):

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